

MODULAR COURSES OF DESIGN FLOW AND IMPLEMENTATION IN PROGRAMMABLE LOGIC DEVICES

The courses can be built freely by connecting different modules of your choice according to needs and background.

On customer request, module contents can be customized and tailored for specific targets of use and application.

MODULES

Introduction to programable logic devices

1 day

Applications with FPGA devices

1 day

Introduction to VHDL

1 day

Advanced use of VHDL

1 day

Introduction to VIVADO suite

1 day

Advanced use of VIVADO suite

1 day

Communication and interfacing 2 days

System-on-chip devices 1 day PL and PS sinergy

1 day

Timing issues and constraints

2 days

Performance optimization of designs for FPGA

2 days

Advanced techniques of implementation in FPGA
2 days



EXAMPLES OF COURSES











LOGISTICS

Dates: Course dates are to be agreed freely with individual customers.

Duration: The duration of the courses is the sum of the days associated with the linked

modules. Each day consists of 8 hours of actual work.

Location: Courses are held at the location chosen by the customer.

Costs: The cost of each course is determined by the number of participants (minimum 1

maximum 10), the total number of days and the location.

Please, contact us for more information and quotations.